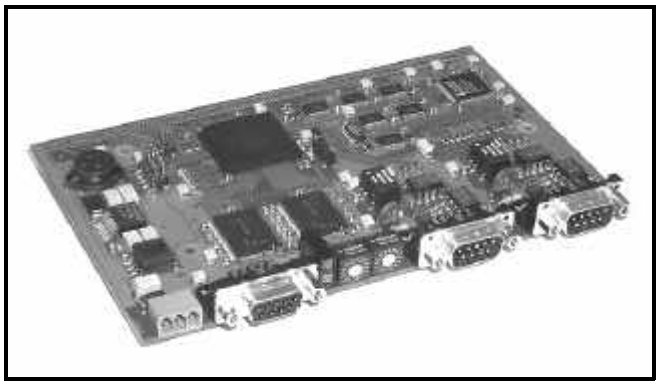
EPPC-555
Embedded PowerPC Board with CAN Interfaces

esd gmbh

- microcontroller MPC555 with in-build FPU
- 2x CAN ISO-11898 electrical isolated
- expansion bus



Powerful Microcontroller

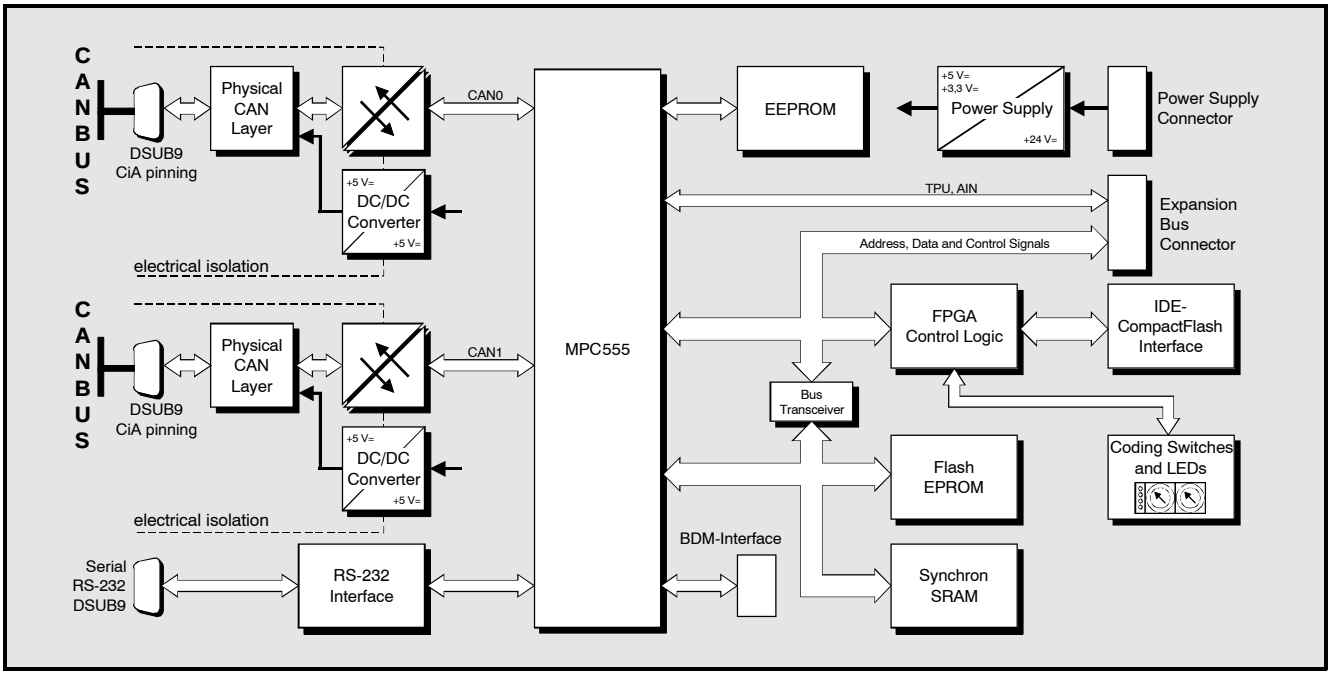
The EPPC-555 is a stand-alone microcontroller board based on the Motorola MPC555. The MPC555 has an integrated 32-bit PowerPC kernel with a clock rate of 40 MHz and a 64-bit FPU.
The board is equipped with 2 Mbyte SRAM, up to 8 Mbyte Flash and a CompactFlash slot to carry various memory cards. A non-volatile I²C-EEPROM is used to store configuration parameters.

Network Interfaces

The EPPC-555 board provides 2 CAN interfaces based on the in-build TOUCAN controllers with transfer rates up to 1 Mbit/s. The interfaces are designed according to ISO 11898 with electrically isolation via high-speed optocouplers and DSUB9 connectors.
The serial RS-232 interface can be connected via a DSUB9 connector, too.

Software Support

The board comes with the multitasking real time operating system RTOS-UH on-board. Drivers for CAN, the CompactFlash interface and the serial interface are included.
Other operating systems are available on request.



Technical Specifications:

Microcontroller and Memory:	
Microcontroller:	*Motorola MPC555, 40 MHz, 32 bit, 64-bit FPU*
Memory:	*512k x 32 bit synchr. SRAM (2 MB) up to 4 MB, 1M x 32 bit Flash EPROM (4 MB) up to 8 MB, I²C-EEPROM*
CompactFlash:	*type I, 3.3 V, 'True IDE' mode, slot internally accessible only*
Interfaces:	
Serial:	*1x RS-232 at DSUB9 female*
CAN:	*2x CAN, controller SJA1000, CAN 2.0A/B, electrically isolated, 1 Mbit/s, ISO11898, DSUB9 male, 2 module-Id coding switches*
DeviceNet: (option)	*acc. to DeviceNet Communication Model and Protocol rev. 2.0*

General:	
Ambient temperature:	*0...50 °C*
Power supply:	*8 VDC ... 34 VDC*
Dimensions:	*100 x 160 mm*

Order information:		
Designation		*order no.*
EPPC-555	*MPC555, 2 MB SRAM, 4 MB Flash, 2x CAN, RS-232*	*I.2002.02*
EPPC-555-RTOS	*RTOS-UH operating system*	*I.2002.35*
EPPC-555-ME	*English user's manual*	*I.2002.21*

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